

UNISONIC TECHNOLOGIES CO., LTD

UF4N20 Preliminary Power MOSFET

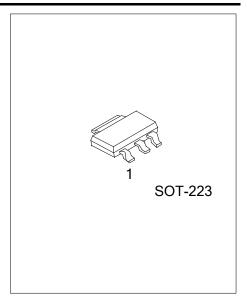
4A, 200V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **UF4N20** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

■ FEATURES

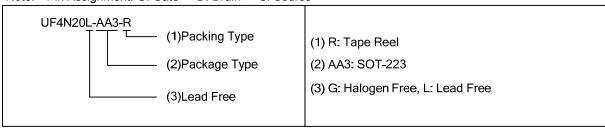
- * $R_{DS(ON)}$ <2 Ω @ V_{GS} =10V, I_D =4A
- * High switching speed
- * Typically 3.2nC low gate charge
- * 100% avalanche tested



■ ORDERING INFORMATION

Ordering	Dookono	Pin Assignment			Deelsing		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UF4N20L-AA3-R	UF4N20G-AA3-R	SOT-223	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	200	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current		I_{D}	4	Α
Avalanche Current		I_{AR}	4	Α
A	Single Pulsed	E _{AS}	52	mJ
Avalanche Energy	Repetitive	E _{AR}	52	mJ
Power Dissipation		P_{D}	0.8	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS M		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	$I_D = 250 \mu A, V_{DS} = 0 V$	200			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =200V			0.95	μA	
Gate-Source Leakage Current	Forward	- I _{GSS}	V_{GS} =+20V, V_{DS} =0V			+95	nA	
	Reverse		V _{GS} =-20V, V _{DS} =0V			-95	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	I _D =250μA	2		4	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =4A			2	Ω	
On State Drain Current		$I_{D(ON)}$	V _{GS} =10V, V _{DS} =10V, f=1MHz	0		30	Α	
DYNAMIC PARAMETERS								
nput Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1MHz			850	pF	
Output Capacitance		Coss				250	pF	
Reverse Transfer Capacitance		C_{RSS}				200	pF	
SWITCHING PARAMETERS								
Total Gate Charge		Q_G	V _{DD} =50V, I _D =4A, I _G =100μA, -V _{GS} =10V		3.2		nC	
Gate to Source Charge		Q_GS			0.64		nC	
Gate to Drain Charge		Q_GD			1.6		nC	
Turn-ON Delay Time		$t_{D(ON)}$			6		ns	
Rise Time		t_R	V_{DD} =30V, I_{D} =4A, R_{G} =25 Ω , V_{GS} =0~10V		38		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$			11		ns	
Fall-Time		t _F			13		ns	
SOURCE- DRAIN DIODE RATIN	NGS AND C	CHARACTERI	STICS					
Maximum Body-Diode Continuous Current		Is				4	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				16	Α	
Drain-Source Diode Forward Voltage		V_{SD}	I _S =4A	0.1		1.48	V	

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